

SRI ADITYA ENGINEERING COLLEGE

(Approved by AICTE & Affiliated to JNTUK, Kakinada)
(Recognized by UGC under section 2(f) of UGC Act 1956)
Aditya Nagar, ADB Road, Surampalem – 533 437.

Dept. Of Electronics & Communication Engineering

- Title of the Workshop/Seminar : A TWO DAY WORKSHOP ON SIGNAL
PROCESSING
- Resource Persons : Dr.N.VENKATESWARULU, PROFESSOR IN ECE
BAPATLA ENGINEERING COLLEGE,
GUNTUR.
- Date(s) of Workshop/Seminar : 16-02-2015 to 17-02-2015
- Aim of Workshop :

Digital signal Processing is an important branch of ECE that deals with the detection and processing of digital signals with reliability and accuracy required for digital communication by employing multiple technique. This workshop involves Discrete Fourier Transform, Fast Fourier Transform, FIR & IIR, Digital signal Processing, Multirate signal processing, Finite word length effects.

Program Schedule:

03-10-2016(Day-1):

Session 1:

- Discrete Fourler Transform

Session 2:

- FAST Fourier Transform

Session 3:

- FIR & IIR

04-10-2016(Day-2):

Session 1:

- Digital Signal Processing

Session 2:

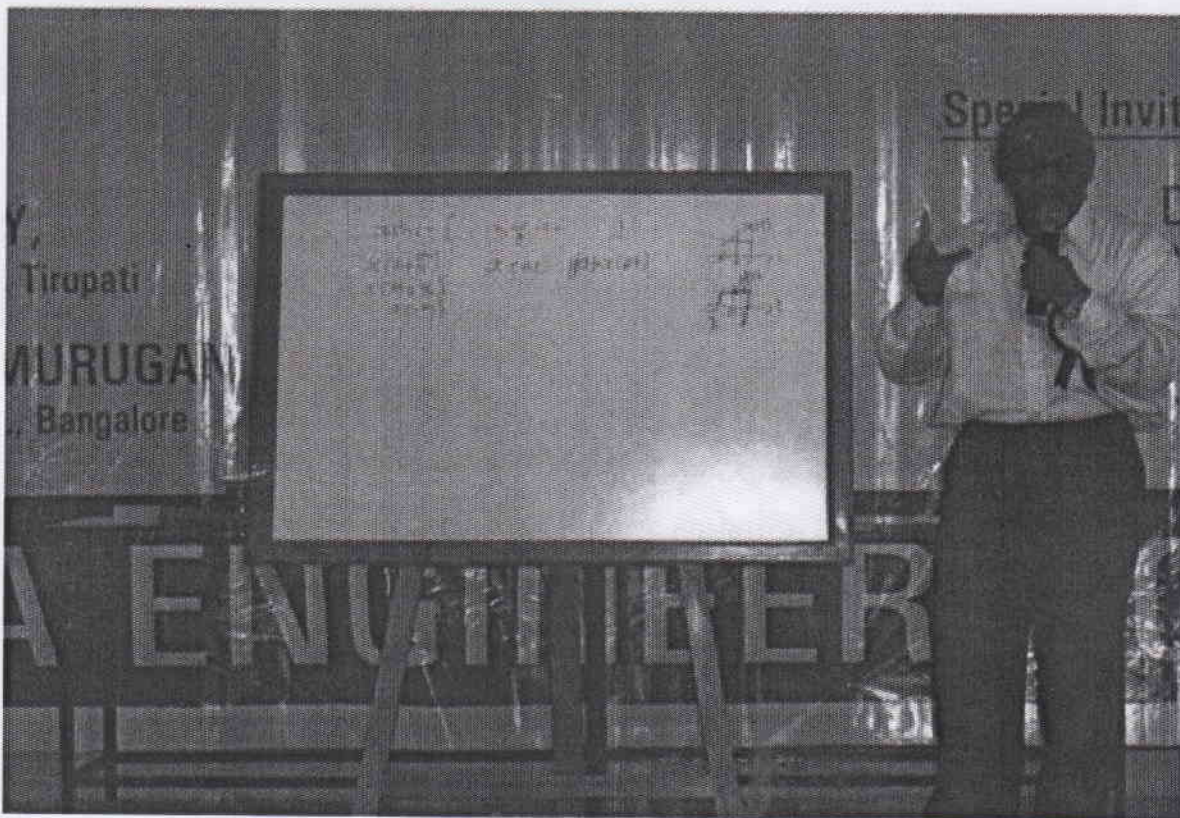
- Multirate Signal Processing

Session 3:

- Finite word length effects

No. of Participants : 48

PROGRAM PHOTOS






Overall Assessment : Good

Event Coordinators :

1. Mr. G RAMA KRISHNA, Professor, Dept. of ECE
2. Mr. P.RAMESH , Assoc.Professor , Dept.of ECE
3. Mr. M.VENKATESWARULU, Assoc.Professor, Dept.of ECE
4. Mrs. N.RADHA, Assoc. Professor, Dept. of ECE
5. Mr. K.SIVA NAGENDRA, Asst. Professor, Dept. of ECE
6. Mr. B.JAGADEESH BABU, Asst. Professor, Dept. of ECE
7. Mrs. Y.SUGANDHINAIDU, Asst. Professor, Dept. of ECE
8. Mr. G.BHEEMESWARARAO, Asst. Professor, Dept. of ECE
9. Mrs. P.JHANSI, Asst. Professor, Dept. of ECE


Signature of the HOD

Head of the Department
Electronics & Communication Engineering
Sri Aditya Engineering College -
SURAMPALEM-533 437


Signature of the Principal

PRINCIPAL
SRI ADITYA ENGINEERING COLLEGE
Surampalem, E.G. Dist.