

ADITYA COLLEGE OF ENGINEERING

(Approved by AICTE & Affiliated to JNTUK, Kakinada)
(Recognized by UGC under section 2(f) of UGC Act 1956)
Aditya Nagar, ADB Road, Surampalem – 533 437.

Dept. Of Electronics & Communication Engineering

Title of the Workshop/Seminar : A TWO DAY WORKSHOP ON IOT
Resource Persons : Mr .KIRAN C BENNUI, APPLICATION
ENGINEER, EDGATE TECHNOLOGIES,
BANGALORE

Date(s) of Workshop/Seminar : 05-08-2016 to 06-08-2016

Aim of Workshop :

This workshop is a very good opportunity to the aspirants who are interested to learn TI-ANALOG SYSTEMS WITH EMPHASIS ON IOT .This workshop covers Introduction to Msp430, Bit Manipulations (Arithmetic). Analog To Digital Conversion, Energia Based Programming. Capacitive Touch Booster pack, Live IOT Project.

Program Schedule:

16-02-2015(Day-1):

Session 1:

- Introduction to VSLI DESIGN

Session 2:

- Growing complexity of modern digital systems and embedded system designs

Session 3:

- System Verilog (SV) Basics

17-02-2015(Day-2):

Session 1:

- Design & Verification using SV

Session 2:

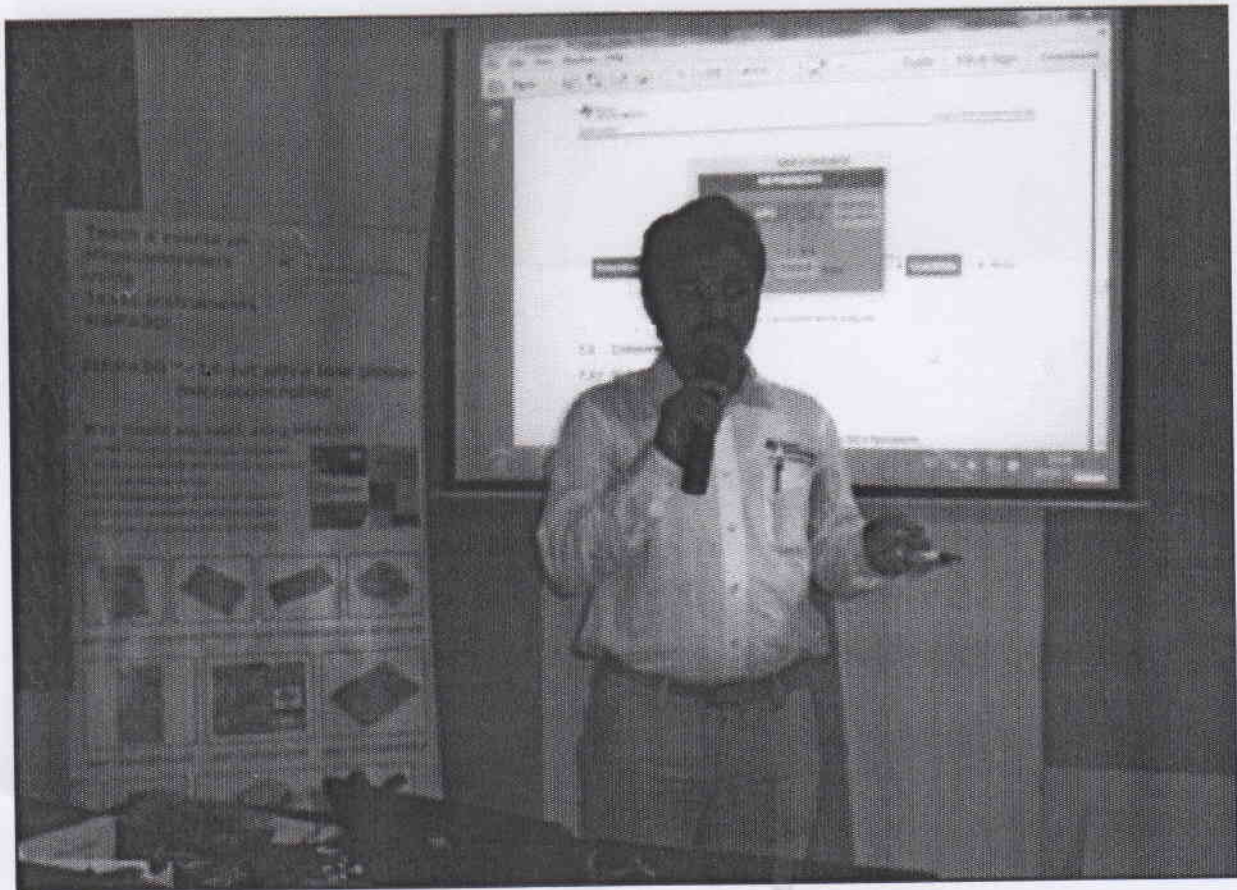
- Creating Verification Environment

Session 3:

- QuestaSim from Mentor Graphics and Case Study Guidelines

No. of Participants : 35

PROGRAM PHOTOS





Overall Assessment : Good

Event Coordinators :

1. Mr. G RAMA KRISHNA, Professor, Dept. of ECE
2. Mr.P.RAMESH, Assoc.Professor , Dept.of ECE
3. Mr.M.VENKATESWARULU, Assoc.Professor,Dept.of ECE
4. Mr. S.RAJA GOPAL, Asst. Professor, Dept. of ECE
5. Mr. M.RAGHUNATH, Asst. Professor, Dept. of ECE
6. Mr. V.KIRAN, Asst. Professor, Dept. of ECE
7. Mrs. P.JHANSI, Asst. Professor, Dept. of ECE
8. Mr. K.MAHESH BABU, Asst. Professor, Dept. of ECE
9. Mrs. Y.SUGANDHI NAIDU, Asst. Professor, Dept. of ECE
10. Mr. D.AVINASH BABU, Asst. Professor, Dept. of ECE
11. Mr. G.BHEEMESWARA RAO Asst. Professor, Dept. of ECE


Signature of the HOD

Head of the Department
electronics & Communications Engineering
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Signature of the Principal

PRINCIPAL
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