

# ADITYA COLLEGE OF ENGINEERING

(Approved by AICTE & Affiliated to JNTUK, Kakinada)  
(Recognized by UGC under section 2(f) of UGC Act 1956)  
Aditya Nagar, ADB Road, Surampalem – 533 437.

## Dept. Of Electronics & Communication Engineering

Title of the Workshop/Seminar : A TWO DAY WORKSHOP CAD TOOLS IN VLSI

Resource Persons : Dr. Ch.Chalapathi Rao , Professor in ECE  
DNR COLLEGE OF ENGINEERING & TECH.

Date(s) of Workshop/Seminar : 17-07-2017 to 18-07-2017

Aim of Workshop :

The workshop on "CAD tools in VLSI" is acquaintance tool in learning new techniques in CAD tools and their scientific advancement in the emerging directions for the department of ECE and EEE. This workshop involves Introduction about VLSI, Different stages in VLSI design flow, Front-end VLSI design, FPGA Design flow, Transistor level design issues, Logic synthesis and static timing Analysis, High – speed circuits and processor architectures, ASIC design flow.

### Program Schedule:

17-07-2017(Day-1):

Session 1:

- Different stages in VLSI design flow

Session 2:

- Front end VLSI design

Session 3:

- FPGA design flow

18-07-2017(Day-2):

Session 1:

- Transistor level design issues

Session 2:

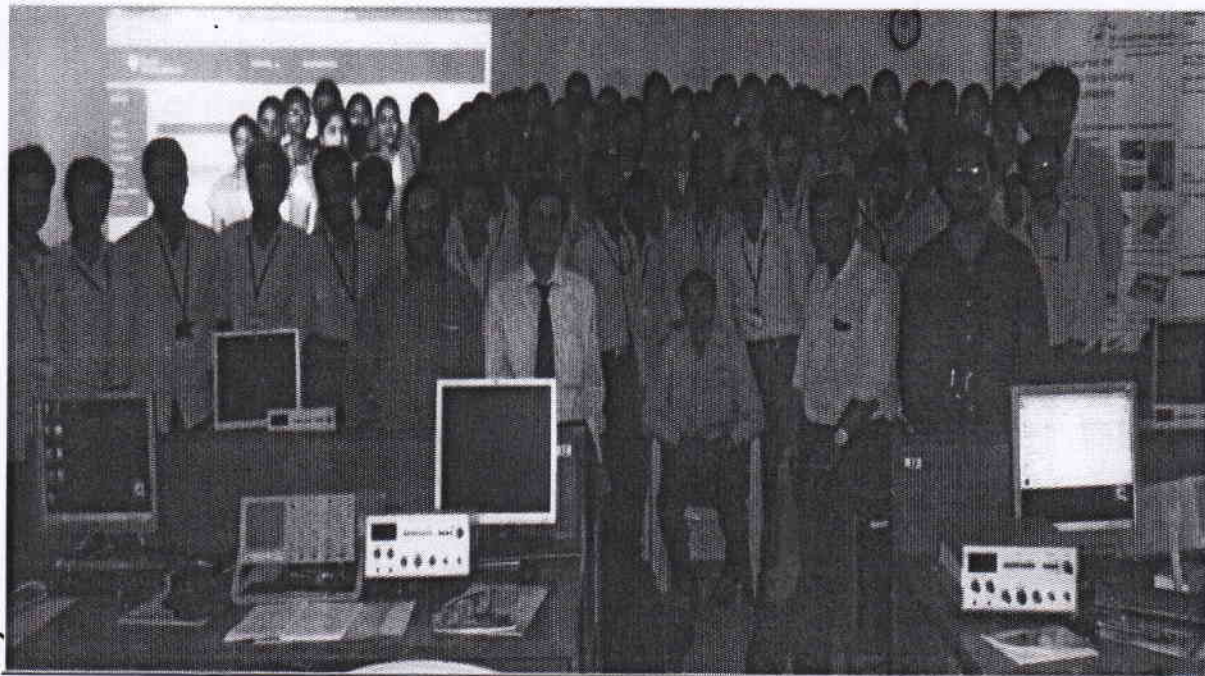
- High speed circuit and processor architecture

Session 3:

- ASIC design flow

No. of Participants : 40

### PROGRAM PHOTOS



Overall Assessment : Good

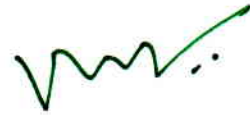
Event Coordinators :

1. Mr. G RAMA KRISHNA, Professor, Dept. of ECE
2. Mrs. M.VEERAPANDU , Assoc.Professor , Dept.of ECE
3. Mr. G.SREEDEVI, Assoc.Professor, Dept.of ECE
4. Mr.B.SRINIVAS, Assoc. Professor, Dept. of ECE
5. Mr.M.S.R.SEKHAR, Assoc. Professor, Dept. of ECE
6. Mr. K.SIVA NAGENDRA, Asst. Professor, Dept. of ECE
7. Mr. B.JAGADEESH BABU, Asst. Professor, Dept. of ECE
8. Mr.T.GANESH REDDY, Asst. Professor, Dept. of ECE
9. Mr. G.BHEEMESWARARAO, Asst. Professor, Dept. of ECE
10. Mr.R.GNANA VIRGIN, Asst. Professor, Dept. of ECE
11. Mr.T.GANESHREDDY, Asst. Professor, Dept. of ECE
12. Ms.B.SAVITRI, Asst. Professor, Dept. of ECE
13. Mr.P.BALA SRINIVAS, Asst. Professor, Dept. of ECE



**Signature of the HOD**

Head of the Department  
electronics & Communications Engineering  
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**Signature of the Principal**

PRINCIPAL  
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